



## บันทึกข้อความ

ส่วนราชการ ภาควิชาวิศวกรรมไฟฟ้าและอิเล็กทรอนิกส์ คณะวิศวกรรมศาสตร์ โทร. 3330

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เรื่อง ขออนุมัติเบิกค่าตอบแทนการตีพิมพ์ผลงานในวารสารวิชาการ

เรียน รองคณบดีฝ่ายวิจัยและบริการวิชาการ ผ่านหัวหน้าภาควิชาวิศวกรรมไฟฟ้า

อ้างถึงประกาศมหาวิทยาลัยอุบลราชธานี คณะวิศวกรรมศาสตร์ เรื่อง หลักเกณฑ์การจ่ายค่าตอบแทนการตีพิมพ์ผลงานในวารสารวิชาการ คณะวิศวกรรมศาสตร์ มหาวิทยาลัยอุบลราชธานี พ.ศ. 2556 ประกาศ ณ วันที่ 12 พฤศจิกายน 2556 ความแจ้งแล้วนั้น

ด้วยบทความวิชาการของข้าพเจ้า นาย ประชา คำภักดี เรื่อง “The Analysis and Control of Zero-Sequence Components in a Transformerless Back-To-Back (BTB) System Using Modular Multilevel Cascade Converters for Power Distribution Systems” ได้รับการตีพิมพ์ใน Engineering Journal ปีที่ 21 ฉบับที่ 6 เดือนตุลาคม 2560 หน้า 81-89 ในกรณีนี้ ข้าพเจ้าใคร่ขออนุมัติเบิกค่าตอบแทนการตีพิมพ์ผลงานในวารสารวิชาการเรื่องดังกล่าว ทั้งนี้ ขอรับรองว่าผลงานดังกล่าวไม่ได้เป็นส่วนหนึ่งของการทำปฏิญานิพนธ์ของผู้ขอ

จึงเรียนมาเพื่อโปรดพิจารณา

(ดร.ประชา คำภักดี)

อาจารย์สังกัดภาควิชาวิศวกรรมไฟฟ้าและอิเล็กทรอนิกส์

เรียน คณบดี

เพื่อโปรดพิจารณา

๒๒

(ผู้ช่วยศาสตราจารย์ ดร.วรการ วงศ์สายเชื้อ)

หัวหน้าภาควิชาวิศวกรรมไฟฟ้าและอิเล็กทรอนิกส์

เรียน รองคณบดีฝ่ายวิจัยและบริการวิชาการ  
ตามที่ ดร.ประชา คำภักดี อาจารย์  
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เรียน รองคณบดีฝ่ายวิจัยและบริการวิชาการ  
16 มี.ค. 61

แบบเสนอขอรับค่าตอบแทนในการตีพิมพ์วารสารวิชาการ

1. เอกสารประกอบการเสนอขอรับค่าตอบแทนในการตีพิมพ์วารสารวิชาการ

1.1 แบบขอรับค่าตอบแทน

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2.2 ชื่อบทความวิจัย

(ภาษาไทย) .....

(ภาษาอังกฤษ) The Analysis and Control of Zero-Sequence Components in a Transformerless Back-To-Back (BTB) System Using Modular Multilevel Cascade Converters for Power Distribution Systems.

2.3 รายละเอียดของวารสาร

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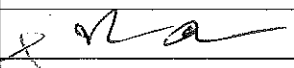
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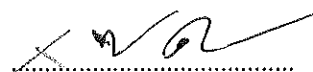
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(ดร.ประชา คำภักดี)

ผู้เสนอขอรับค่าตอบแทน

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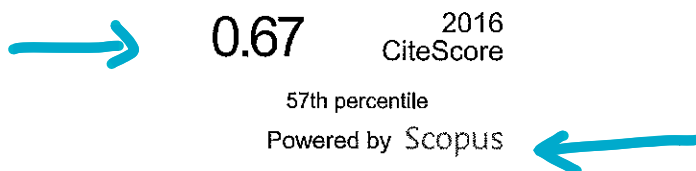
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*Article*

## The Analysis and Control of Zero-Sequence Components in a Transformerless Back-To-Back (BTB) System Using Modular Multilevel Cascade Converters for Power Distribution Systems

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**Abstract.** This paper provides an intensive discussion on analysis and simulation of a back-to-back (BTB) system based on double-star chopper cells (MMCCs-DSCCs). It is installed between two 6.6-kV power distribution feeders. It is also referred to as the so-called “loop power flow controller.” A three-phase 200-V, 10-kW, 50-Hz BTB transformerless system is simulated to verify its operating principles, modelling, and performance of zero-sequence current control using combination between a simple PI controller and a common-mode choke. The simulation results show that the zero-sequence current circulating between the two feeders can be suppressed as small as 10 mA (0.03%) in rms, which is within acceptable value and small enough to negligible.

**Keywords:** Back-to-back (BTB) systems, distributed power generators, loop power flow controller, modular multilevel cascade converters (MMCCs), zero-sequence components.

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## 1. Introduction

The power generation based on renewable energy is often called as distributed power generators (DGs) in contrast to centralized power generators because most of them are installed on power distribution systems [1]. Furthermore, in the future, an increasing part of the installed capacity will be connected to the DGs, which poses additional challenges to planning and safe operation of the systems [2]. In addition, flexible AC transmission system (FACTS) devices for control of power flow, voltage, and frequency are used to increase transmission capacity, reduce congestion, improve controllability and integrate more renewable energy into grids. For traditional two-level converters used in the above applications, step-up transformers and/or zigzag transformers are used to reach the medium and high voltage levels and to synthesize output voltage to multi-pulse waveforms [3]. However, transformers are bulky, lossy, high cost and present nonlinear/saturation problems that lead to difficulties in the system control.

Recently, attention has been paid to the “modular multilevel converter” (MMC) [4, 5] for high-voltage and high-power applications such as long-distance high-voltage direct-current (HVDC) transmission systems. According to the classification of modular multilevel cascade converters in [6], an MMCC using chopper cells is referred to as a double-star chopper cells (DSCC). Research scientist and engineers in power electronics and power systems have presented or published technical papers on DSCC-based power conversion systems with focus on control, modelling, analysis, design, and/or operation [7-11]. The “HVDC-Plus” from Siemens [12], “HVDC-Light” from ABB [13], and “HVDC-Maxsine” from Alstom [14] are examples of the implementation of DSCC concept in applications of VSC-HVDC transmission. Some papers in the literature have focused on control, modelling, and performance of the DSCC-based BTB or HVDC systems during the voltage sags using different control strategies for mitigating and controlling the zero-sequence current components in the systems, especially in case of transformerless system [15].

The aim of this paper following [15] is to analyse and control the zero-sequence components in details when the BTB system is installed between two 6.6-kV power distribution feeders with many distributed power generators. It also referred to as the so-called “loop power flow controller.” A simple modelling is used for simulation by the software package “PSCAD/EMTDC” to verify the validity and effectiveness of the zero-sequence current control techniques for the DSCC-based BTB system.

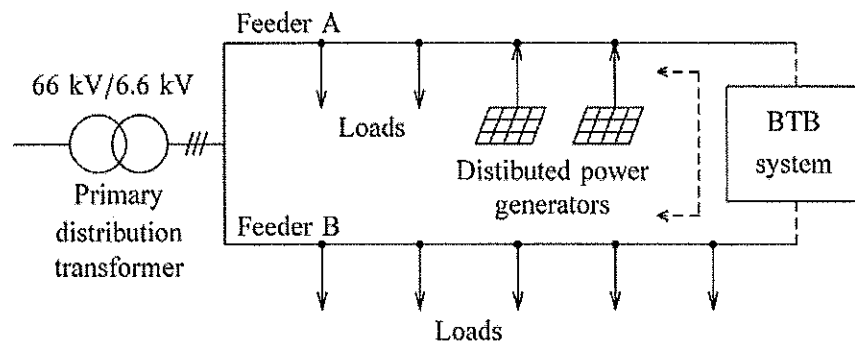


Fig. 1. A 6.6-kV power distribution system consisting of two radial feeders equipped with a DSCC-based BTB system [1].

## 2. Circuit Configuration and Equations

### 2.1. Circuit Configuration

Figure 2(a) shows the circuit configuration for a transformerless DSCC-based BTB system used in experiments and simulations. Table 1 summarizes the circuit parameters of Fig. 2(a). Figure 2(a) is a simplified model of Fig. 1, in which the following reasonable assumptions are made:

- The line-frequency transformer is modelled by an ideal voltage source presenting no background system inductance and leakage inductance;

- The impedances in feeder A and feeder B are neglected, while two ac-link inductors are used to connect the feeders and the BTB system;
- The currents flowing to the loads or flowing out from the distributed power generators are set to zero.

Table 1. Circuit parameters of Fig. 2.

Rated power of the DSCCs	$P$	10 kW
Nominal line-to-line rms voltage	$V_S$	200 V
Nominal line frequency	$f_S$	50 Hz
Cascade count per leg	$n$	16
AC-link inductor	$L_{ac}$	2 mH (16%)*
Center-tapped inductor	$L_Z$	3 mH (24%)*
DC-link voltage reference	$V_{dc}^*$	400 V
DC-capacitor of each chopper cell	$C$	6.6 mF
DC-capacitor voltage reference	$V_C^*$	50 V (= 400 V/8)
Unit capacitance constant	$H_C$	40 ms at 50 V
Triangular carrier frequency	$f_C$	450 Hz
Equivalent carrier frequency	$16f_C$	7.2 kHz
Dead time (blanking time)	$T_d$	8 $\mu$ s
Common-mode choke at common dc link. 400 V, 25 A	$L_{CM}$	77 mH at 3.6 kHz (113 mH at 50 Hz)

\* on a three-phase, 200-V, 10-kW, 50-Hz base.

## 2.2. Circuit Equations

Figure 2(a) shows the power circuit configuration of the DSCC-based BTB system with a per-leg chopper-cell count of 16 producing 17-level (33-level in line-to-line) pulse width modulation (PWM) waveforms [16]. The chopper-cell uses a dc capacitor and two IGBTs that form a bidirectional chopper as shown in Fig. 2(b). Here,  $v_u$  is the low-voltage side voltage of each chopper-cell. In Fig. 2(a), a couple of identical DSCCs, “DSCC-A” and “DSCCB” are connected back-to-back without any common dc-link capacitor. Each DSCC consists of 48 chopper cells depicted in Fig. 2(b), and three-center-tapped inductor in Fig. 2(c). In Fig. 2(a),  $i_{puA}$  and  $i_{nuA}$  are the positive and negative arm currents,  $i_{suA}$  is the supply current, and  $i_{zuA}$  is the circuit current along the u-phase leg of DSCC-A. Hence, the following equations exist among the four currents:

$$i_{zuA} = \frac{1}{2}(i_{puA} + i_{nuA}), \quad (1)$$

$$i_{puA} = -\left(\frac{i_{suA}}{2} + i_{zuA}\right), \quad (2)$$

$$i_{nuA} = \left(\frac{i_{suA}}{2} + i_{zuA}\right). \quad (3)$$



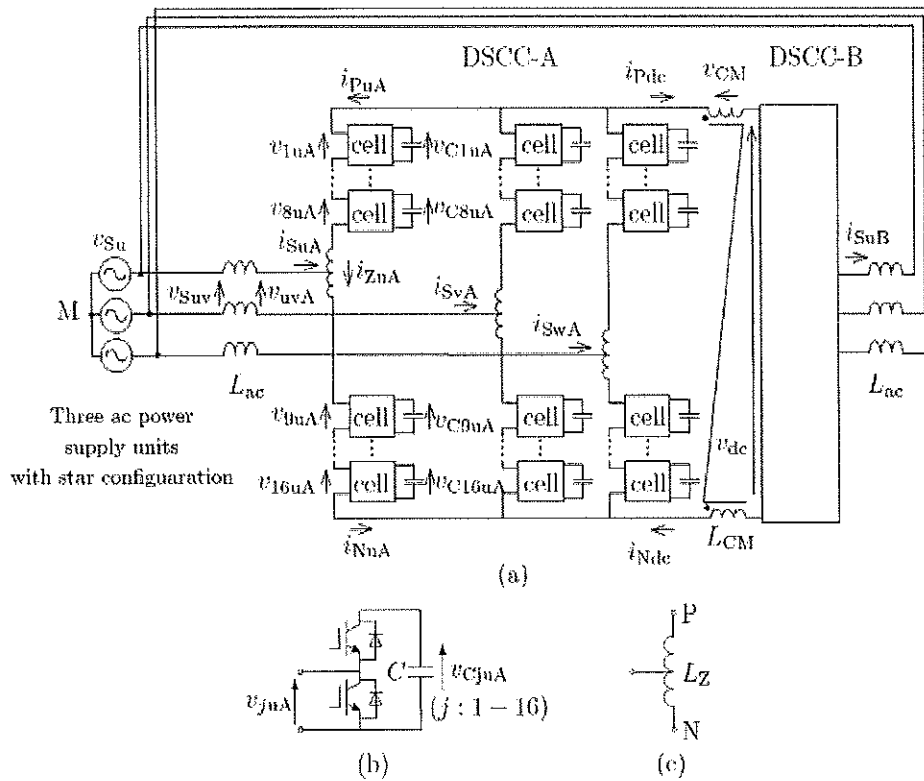


Fig. 2. Circuit configuration for the transformerless DSCC-based BTB system with a cascade of  $n = 16$ . (a) Main circuit. (b) Chopper cell. (c) Center-tapped inductor [15].

### 3. Modelling and Zero-Sequence Components

A zero-sequence current  $i_0$  may flow when two load ends of feeder A and feeder B are connected by a transformerless BTB system, in which  $i_0$  is defined as

$$i_0 = \sum_{x=u,v,w} i_{Px} = (- \sum_{x=u,v,w} i_{Px} + \sum_{x=u,v,w} i_{Nx}) \quad (4)$$

$$i_0 = i_{Pdc} - i_{Ndc},$$

where  $i_{Pdc}$  and  $i_{Ndc}$  are dc-link currents at positive bus and negative bus, respectively.

Figure 3 shows an equivalent circuit with a focus on zero-sequence components derived in [1]. Here, each arm is represented by a voltage source (e.g.,  $v_{PuA}$  or  $v_{NuA}$ ), which is the sum of low-voltage-side voltages of the corresponding chopper cells. It should be noted that  $i_0$  circulates through feeder A and feeder B, and no zero-sequence current flows in the common ac mains because the 6.6-kV utility distribution systems in Japan are based on a three-phase ungrounding system. In addition, the background system inductance  $L_s$  does not affect  $i_0$  because no zero-sequence current flows in  $L_s$ . Figure 3(a) can be changed to Fig. 3(b), in which three wires at the ac sides are summarized to a single wire. Each of six legs in Fig. 3(b) produces the same amount of zero-sequence voltage. Figure 3(b), therefore, is changed to Fig. 3(c), where  $v_0$  is a zero-sequence voltage produced by each arm. The detail on how to derive Fig. 3(c) is given in [1].

Figure 4 shows the block diagrams of power control and zero-sequence current control used in DSCC-A. The power control is achieved by the conventional decoupled current control and the detail is given in [14]. The zero-sequence current  $i_0$  contains low-frequency components less than 1 Hz and switching-ripple components around several kilo-hertz. Ideally,  $i_0$  contains no low-frequency components less than 1 Hz. However, they occur in the actual system due to the effect of passive-component tolerances, lead-wire resistances, tolerances in switching characteristics, and so on. This low-frequency component should be suppressed by applying the conventional PI (proportional-plus-integral) control, in which  $v_0^*$  is the

common voltage reference which is determined by the feedback control of  $i_0$  and its reference  $i_0^* = 0$  as shown in Fig. 4. On the other hand, the switching-ripple components around several kilo-hertz should be suppressed by using the appropriately-designed common-mode choke  $L_{CM}$  connected to the common dc link [1].

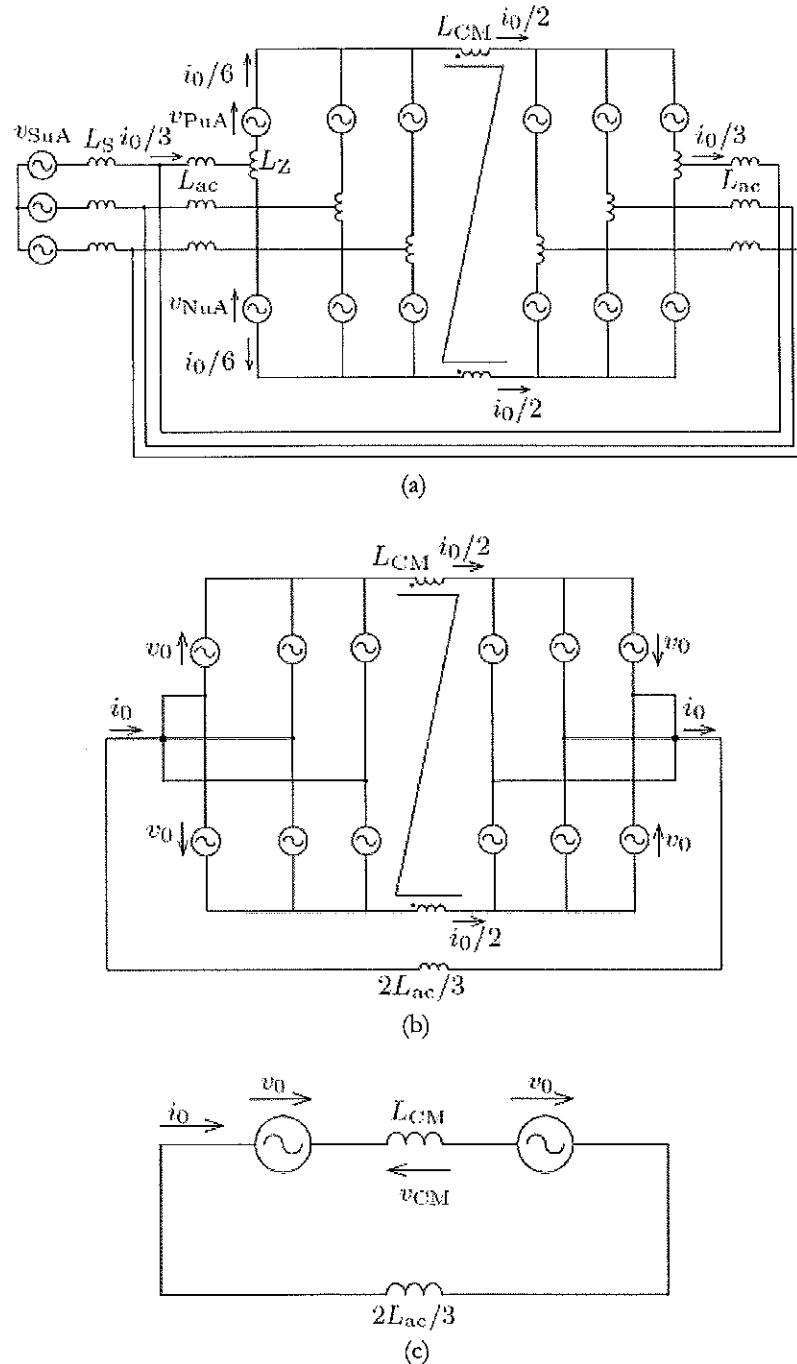


Fig. 3. Equivalent circuits for zero-sequence components [1].

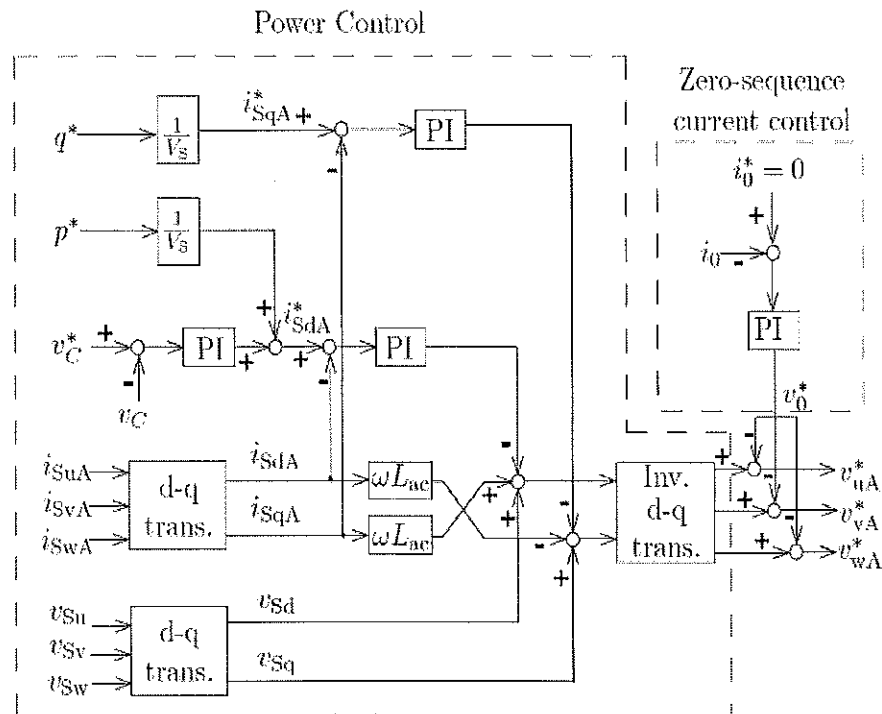


Fig. 4. Block diagrams of power control and zero-sequence current control used in DSCC-A [15].

#### 4. Simulation and Analysis Results

Figure 5 shows simulated waveforms of the zero-current components during normal voltage condition, where  $p^* = 10 \text{ kW}$  and  $q_A^* = q_B^* = 0 \text{ kVA}$ . Figure 5(a) shows the zero-sequence voltage of DSCC-A,  $v_{0A}$  and that of DSCC-B,  $v_{0B}$ . Here,  $v_{0A}$  is not equal to  $v_{0B}$ . The zero-voltage component  $v_0$  is 7.3 V in rms as shown in Fig. 5(b), while the voltage across common-mode choke  $v_{CM}$  is 10.2 V in rms as shown in Fig. 5(c). Figure 5(d) shows the three waveforms of zero-sequence current  $i_0$  under different conditions such as: (1) No PI-control and No common-mode choke, (2) PI-control and No common-mode choke, and (3) PI-control and common mode choke. As can be seen in Fig. 5(d),  $i_0$  consists of mainly dc component, low frequency components and high-switching ripple components. The dc and low-frequency components can be suppressed by using the PI-controller in which the rms value of  $i_0$  is decreased from 1.86 A (No PI-control) to 0.5 A (with PI-control). However, the PI-control cannot suppress the high-frequency components. Hence, the combination of feedback control and connecting a common-mode choke to the common dc link is required to mitigate the zero-sequence current circulating consisting of low and high frequency components. The blue-line waveform in Fig. 5(d) shows that the zero-sequence current can be suppressed as small as 10 mA (0.03%) in rms, which is small enough to be negligible, compared to the rated feeder current 30 A ( $= 10 \text{ kW} / \sqrt{3} \times 200 \text{ V}$ ). Note that in the actual system, the zero-sequence current should be controlled less than 200 mA (0.06%) of the rated capacity of the feeder 300 A to avoiding malfunction of grounding-detection relays [16].

#### 5. Conclusion

This paper has described an application of a transformerless modular multilevel cascade converter based on a double-star chopper-cells (MMCC-DSCC). It is used to form a BTB (Back-To-Back) system installed between two 6.6-kV power distribution feeders working as the loop power flow controller. The simple modelling of zero-sequence components have been done for analysis and control. Simulation results have confirmed the effectiveness of zero-sequence current suppression methods of PI control and common-mode choke. The zero-sequence current circulating between the two feeders can be suppressed as small as

0.01 A in rms at the rated dc-link current 25 A or 0.03% in rms of rated feeder current 30 A, which is within acceptable value and small enough to negligible.

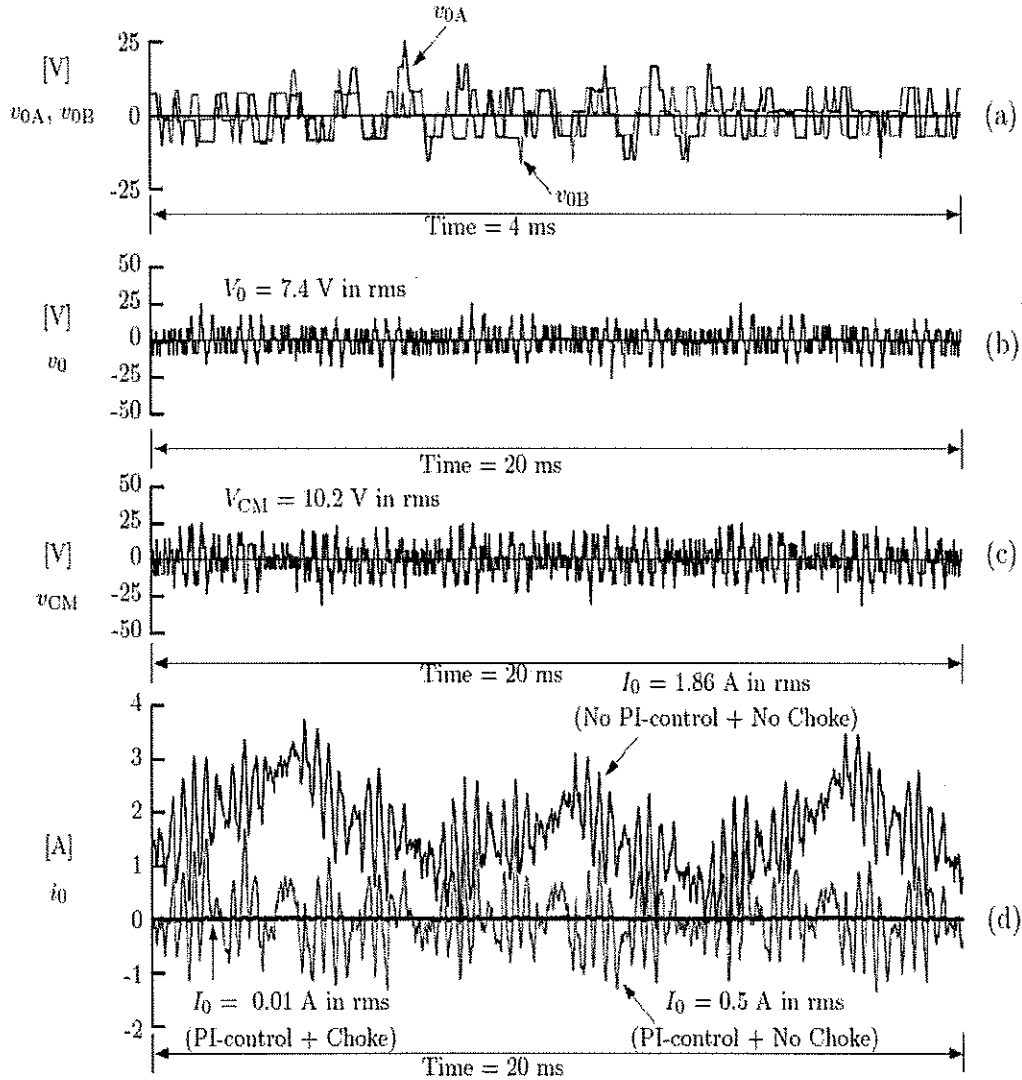


Fig. 5. Simulation waveforms of the zero-sequence components during normal voltage condition, where  $p^* = 10$  kW and  $q_A^* = q_B^* = 0$  kVA: (a) Comparison between zero-sequence voltage of DSCC-A,  $v_{0A}$  and that of DSCC-B,  $v_{0B}$ ; (b) Zero-sequence voltage  $v_0$  ( $= v_{0A}$ ); (c) Voltage across common-mode choke  $v_{CM}$ ; (d) Zero-sequence current  $i_0$ , under different conditions.

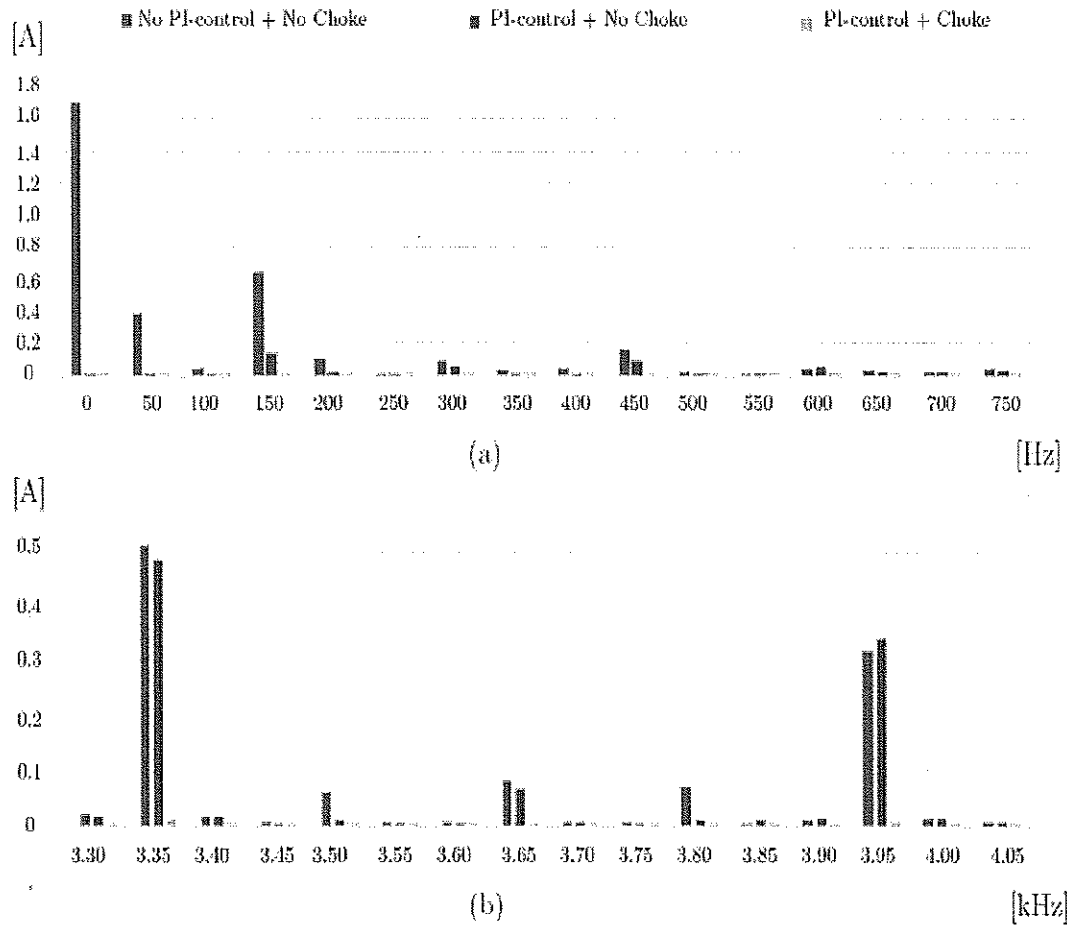


Fig. 6. Frequency spectra of zero-sequence current ( $i_0$ ) in Fig. 5: (a) Low-frequency band; (b) High-frequency band.

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